

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**  
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.  
14669

In R Application Of: Yasuteru ARAYA, et al

MAY 27 2003

Serial No.  
09/872,091

Filing Date  
JUNE 1, 2001

Examiner  
Unassigned

Group Art Unit  
2122

Title: **BUS PERFORMANCE EVALUATION METHOD FOR ALGORITHM DESCRIPTION**

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Address to:  
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P.O. Box 1450  
Alexandria, VA 22313-1450

Technology Center 2100

**37 CFR 1.97(b)**

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

**37 CFR 1.97(c)**

2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:
- ☐ the statement specified in 37 CFR 1.97(e);
- OR**
- ☐ the fee set forth in 37 CFR 1.17(p).

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**Payment of Fee**

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(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of \_\_\_\_\_ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. 19-1013/SSMP as described below.
- ☐ Charge the amount of \_\_\_\_\_
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I certify that this document and fee is being deposited on 05/23/03 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature of Person Mailing Correspondence

Michelle Mustafa

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Dated: May 23, 2003

Signature

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PJE:my

CC:



PATENT

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5/31/03  
SRN

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicants:** Yasuteru ARAYA, et al  
**Serial No.:** 09/872,091  
**Filed:** June 1, 2001  
**For:** BUS PERFORMANCE EVALUATION  
METHOD FOR ALGORITHM DESCRIPTION

**Examiner:** Unassigned  
**Art Unit:** 2122  
**Docket:** 14669  
**Dated:** May 23, 2003

Commissioner for Patents  
Alexandria, VA 22313-1450

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MAY 29 2003

**INFORMATION DISCLOSURE STATEMENT**

Technology Center 2100

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the following references, which are also listed on the attached Form PTO-1449, be made of record in the above-identified case:

1. Ernst, R. et al., "Hardware-Software Cosynthesis for Microcontrollers", IEEE Design & Test of Computers, IEEE, US, Vol. 10, No. 4; Pgs 64+65-75, December 1, 1993;
2. Temam, O. et al., "Software Assistance for Data Caches" Future Generations Computers Systems, Elsevier Science Publishers, NL, Vol. 11, No. 6, October 1, 1995; Pgs. 519-536;
3. Herrmann, D. et al., "An Approach to the Adaptation of Estimated Cost Parameters in the COSYMA System", Hardware/Software Codesign, 1994; Proceedings of the Third International Workshop on Grenoble, France, Sept. 22-24, 1994, Pgs. 100-107; and

**CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Box 1450, Alexandria, VA 22313-1450 on May 23, 2003.

Dated: May 23, 2003

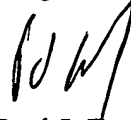
  
Michelle Mustafa

4. Cosyma Architecture and Input Languages, "Online", 15 October 1998 (1998-10-15)  
XP002230336.

The references were cited in a Search Report received from the European Patent Office, dated March 6, 2003. Applicants are submitting copies of the above-cited references, together with a copy of the Search Report. The relevance of the above-identified references has been described in the Search Report.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b) no statement or fee is required.

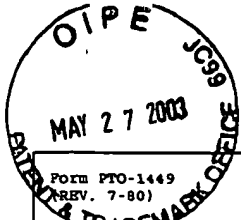
Respectfully submitted,



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Form PTO-1449  
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

Atty. Docket No. 14669

Serial N . 09/872,091

**LIST OF PRIOR ART  
CITED BY APPLICANT**

Applicant Yasuteru ARAYA, et al.

(Use several sheets if necessary)

Filing Date June 1, 2001

Group 2122

**U.S. PATENT DOCUMENTS**

EXAMINER'S INITIAL*	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (if appropriate)
						RECEIVED
						MAY 29 2003
						Technology Center 2100

**FOREIGN PATENT DOCUMENTS**

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	1							
	2							
	3							

**OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

	1	Ernst, R. et al., "Hardware-Software Cosynthesis for Microcontrollers", IEEE Design & Test of Computers, IEEE, US, Vol. 10, No. 4; Pgs 64+65-75, December 1, 1993
	2	Temam, O. et al., "Software Assistance for Data Caches", Future Generation Computer Systems, Elsevier Science Publishers, NL, Vol. 11, No. 6, October 1, 1995; Pgs. 519-536
	3	Herrmann, D. et al., "An Approach to the Adaptation of Estimated Cost Parameters in the COSYMA System", Hardware/Software Codesign, 1994; Proceedings of the Third International Workshop on Grenoble, France, Sept. 22-24, 1994, Pgs. 100-107
	4	Cosyma Architecture and Input Languages, "Online", 15 October 1998 (1998-10-15) XP002230336

EXAMINER

DATE CONSIDERED

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.